

General Description

The MAX16803 current regulator operates from a 6.5V to 40V input-voltage range and delivers up to a total of 350mA to one or more strings of high-brightness LEDs (HB LEDs). The output current of the MAX16803 is adjusted by using an external current-sense resistor in series with the LEDs. A dimming input allows wide-range "pulsed" PWM operation. Wave-shaping circuitry reduces EMI. The differential current-sense input increases noise immunity. The MAX16803 is well suited for applications requiring high-voltage input and is able to withstand automotive load-dump events up to 40V. An on-board pass element minimizes external components while providing $\pm 3.5\%$ output-current accuracy. Additional features include a 5V regulated output and short-circuit and thermal protection.

The MAX16803 is available in a thermally enhanced, $5mm \times 5mm$, 16-pin TQFN package and is specified over the automotive -40°C to +125°C temperature range.

Applications

Automotive Interior: Map, Courtesy, and Cluster Lighting

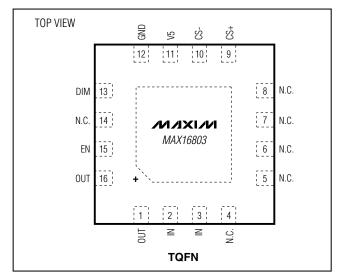
Automotive Exterior: Rear Combination Lights (RCLs)

Emergency Vehicle Warning Lights

Navigation and Marine Indicators

General Lighting

Signage, Gasoline Canopies, Beacons



Pin Configuration

_____Features

- +6.5V to +40V Operating Range
- Adjustable LED Current (35mA to 350mA)
- ±3.5% LED Current Accuracy
- High-Voltage DIM Pin for Dimming Interface
- Integrated Pass Element with Low-Dropout Voltage (0.5V typ)
- +5V Regulated Output with 4mA Source Capability
- Parallel Operation for LED Current > 350mA
- Differential LED Current Sense
- Low Shutdown Supply Current (12µA typ)
- Low 204mV Current-Sense Reference Reduces Power Losses
- Wave-Shaped Edges Reduce Radiated EMI During PWM Dimming
- Thermal Shutdown
- Output Short-Circuit Protection
- Available in Small, Thermally Enhanced, 5mm x 5mm, 16-Pin TQFN Package
- ♦ -40°C to +125°C Operating Temperature Range

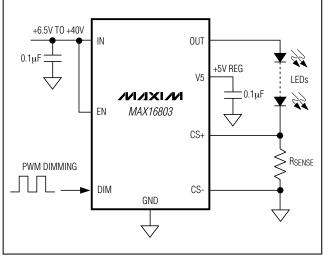
Ordering Information

| PART | TEMP RANGE | PIN- PACKAGE | PKG CODE |
|--------------------|-----------------|-----------------|-------------|
| MAX16803ATE+ | -40°C to +125°C | 16 TQFN-EP* | T1655-3 |
| , Donoton load fro | | | |

+Denotes lead-free package.

*EP = Exposed pad.

Simplified Diagram



Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

| IN, OUT, DIM, and EN to GND | 0.3V to +45V |
|-------------------------------------|----------------|
| CS+, CS-, V5 to GND | 0.3V to +6V |
| OUT Short Circuited to GND Duration | |
| | CO mainsuita a |

| 16-Pin TOFN (| (derate 33 3mW/ | $^{\circ}$ C above +70 | °C)2666.7mW |
|---------------|------------------|------------------------|-------------|
| | (uorato 00.0mm)/ | 0 40010 170 | 0, |

| Operating Junction Temperature Range | 40°C to +125°C |
|--------------------------------------|----------------|
| Junction Temperature | +150°C |
| Storage Temperature Range | 65°C to +150°C |
| Lead Temperature (soldering, 10s) | +300°C |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{IN} = V_{EN} = +12V, C_{V5} = 0.1\mu$ F to GND, $I_{V5} = 0, V_{CS-} = 0V, V_{DIM} = +4V$, connect $R_{SENSE} = 0.58\Omega$ between CS+ and CS- (Note 1). $T_J = -40^{\circ}$ C to $+125^{\circ}$ C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}$ C.) (Note 1)

| PARAMETER | SYMBOL | CONDITIONS | MIN | ТҮР | MAX | UNITS |
|---------------------------|-------------------|---|------|------|------|-------|
| Supply Voltage Range | VIN | (Note 2) | 6.5 | | 40.0 | V |
| Ground Current | lG | I _{LOAD} = 350mA | | 1.4 | 3 | mA |
| Shutdown Supply Current | ISHDN | EN ≤ 0.6V | | 12 | 40 | μA |
| Guaranteed Output Current | IOUT | $R_{SENSE} = 0.55\Omega$ | 350 | | | mA |
| Output Current Accuracy | | 35mA < I _{OUT} < 350mA | | ±3.5 | | % |
| Description (Marker 0) | 11/00 | I_{OUT} = 350mA (current pulsed), 12V < V _{IN} < 40V | | 0.5 | 1.2 | v |
| Dropout Voltage (Note 3) | ΔVDO | I_{OUT} = 350mA (current pulsed), 6.5V < V _{IN} < 12V | | 0.5 | | |
| | | Current rising, DIM rising to 4V | | 17 | | |
| Output Current Slew Rate | | Current falling, DIM falling to 0.6V | | 17 | | mA/µs |
| Short-Circuit Current | | $V_{OUT} = 0V, V_{IN} = 12V$ | 400 | 600 | 800 | mA |
| LOGIC INPUT | | | | | | |
| EN Input Current | I _{EN} | | | | 100 | nA |
| EN Input Voltage High | VIH | | 2.8 | | | V |
| EN Input Voltage Low | VIL | | | | 0.6 | V |
| Turn–On Time | ton | EN rising edge to 90% of OUT | | 80 | | μs |
| CURRENT SENSE (Note 4) | | | | | | |
| Regulated RSENSE Voltage | V _{RSNS} | V _{SENSE} = V _{CS+} - V _{CS-} | 196 | 203 | 210 | mV |
| CS- Voltage Range | | | -0.3 | | +4.1 | V |
| Input Current (CS+) | | $V_{CS+} = 220 mV$ | | | +14 | μA |
| Input Current (CS-) | | $V_{CS+} = 220 mV$ | -84 | | | μA |
| DIM Input Current | | | | | 1 | μA |
| DIM Input Voltage High | VIH | | 4 | | | V |
| DIM Input Voltage Low | VIL | | | | 0.6 | V |
| Turn-On Time | ton | After DIM rising to 4V (Note 5) | | | 52 | μs |
| Turn-Off Time | tOFF | After DIM falling to 0.6V (Note 5) | | | 38 | μs |

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{IN} = V_{EN} = +12V, C_{V5} = 0.1\mu$ F to GND, $I_{V5} = 0, V_{CS-} = 0V, V_{DIM} = +4V$, connect $R_{SENSE} = 0.58\Omega$ between CS+ and CS- (Note 1) $T_J = -40^{\circ}C$ to $+125^{\circ}C$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.) (Note 1)

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
|------------------------------|--------|------------------|-----|------|-----|-------|
| THERMAL OVERLOAD | | | | | | |
| Thermal-Shutdown Temperature | | | | +155 | | °C |
| Thermal-Shutdown Hysteresis | | | | 23 | | °C |
| +5V REGULATOR | | | | | | |
| Output Voltage Regulation | V5 | (Note 6) | 5.0 | 5.27 | 5.5 | V |
| V5 Short-Circuit Current | | V5 = 0V (Note 7) | | 16 | | mA |

Note 1: All devices 100% production tested at $T_A = +25^{\circ}C$. Limits over the operating temperature range are guaranteed by design.

Note 2: Resistors were added from OUT to CS+ to aid with the power dissipation during testing.

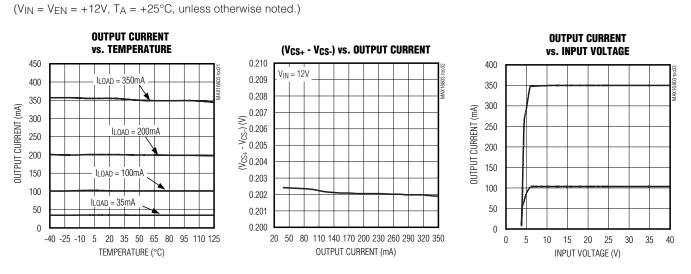
Note 3: Dropout is measured as follows:

Connect R_O = 27 Ω from OUT to CS+. Connect R_{SENSE} = 0.58 Ω from CS+ to CS-. Set V_{IN} = +12V (record V_{OUT} as V_{OUT}). Reduce VIN until VOUT = 0.97 x VOUT1 (record as VIN2 and VOUT2). Δ VDO = VIN2 - VOUT2.

Note 4: Iv5 = 0mA.

Note 5: toN time includes the delay and the rise time needed for IOUT to reach 90% of its final value. toFF time is the time needed for I_{OUT} to drop below 10%. See the Typical Operating Characteristics. t_{ON} and t_{OFF} are tested with 13Ω from OUT to CST. Note 6: Current regulation varies with V5 load (see the Typical Operating Characteristics).

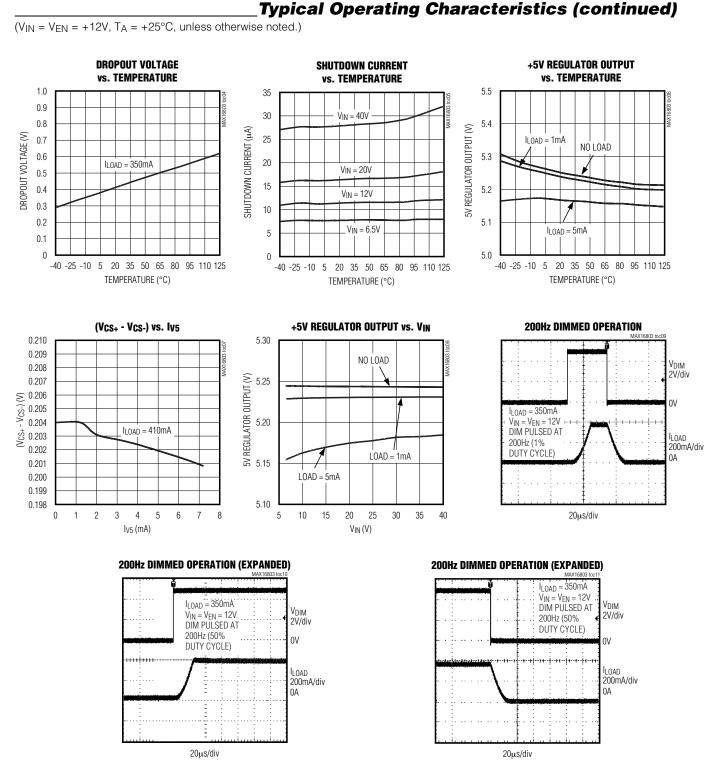
Note 7: Thermal shutdown does not function if the output of the 5V reference is shorted to ground.



Typical Operating Characteristics

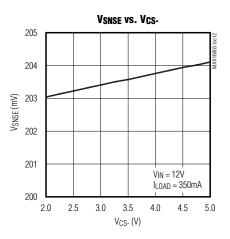
///XI///

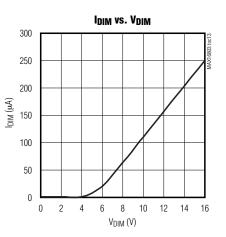




Typical Operating Characteristics (continued)

($V_{IN} = V_{EN} = +12V$, $T_A = +25^{\circ}C$, unless otherwise noted.)



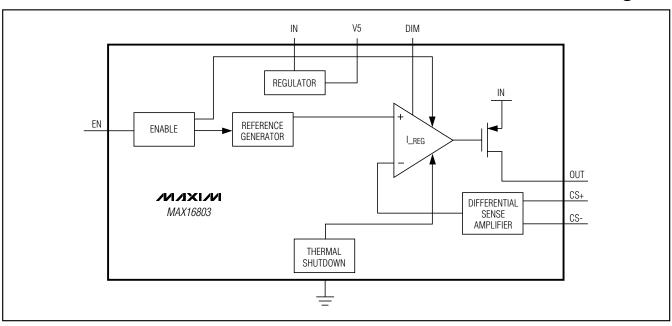


MAX16803

Pin Description

| PIN | NAME | FUNCTION |
|---------|------|--|
| 1, 16 | OUT | Current-Regulated Output. Connect pin 1 to pin 16. |
| 2, 3 | IN | Positive Input Supply. Bypass IN with a 0.1µF (min) capacitor to GND. Connect pin 2 to pin 3. |
| 4–8, 14 | N.C. | Not Internally Connected |
| 9 | CS+ | Positive Input of the Internal Differential Amplifier. Connect the current-sense resistor between CS+ and CS- to program the output current level. |
| 10 | CS- | Negative Input of the Internal Differential Amplifier. Connect the current-sense resistor between CS- and CS+ to program the output current level. |
| 11 | V5 | +5V Regulated Output. Connect a 0.1µF capacitor from V5 to GND. |
| 12 | GND | Ground |
| 13 | DIM | Pulsed Dimming Input. Drive DIM low to disable the output. Drive DIM high to enable the output. Must not be left unconnected. |
| 15 | EN | Enable Input. Drive EN high to enable the output. |
| | EP | Exposed Pad. Connect to the ground plane for effective power dissipation. Do not use as the only ground connection. |

Functional Diagram



Detailed Description

The MAX16803 is a high-current regulator capable of providing up to a total of 350mA of current to one or more strings of HB LEDs. A wide operating input voltage range of +6.5V to +40V makes the MAX16803 ideal for automotive applications. A +5V regulated output provides up to 4mA of current to power external circuitry. In addition, the MAX16803 features thermal and output short-circuit protection. The wide operating voltage range helps protect the MAX16803 against large transients such as those found in load-dump situations up to 40V.

The MAX16803 uses a feedback loop to control the output current. The differential voltage across the sense resistor is compared to a fixed reference voltage, and the error is amplified to serve as the drive to the internal power series pass device (see the *Functional Diagram*). The regulation point is factory-set at (V_{CS+} - V_{CS-}) = 203mV ±3.5%. The regulated current is user defined by the value of RSENSE.

The MAX16803 is a current controller internally optimized for driving the impedance range expected from one or more HB LEDs.

+5V Regulator

The MAX16803 includes a fixed +5V output regulator that delivers up to 4mA of load current throughout the +6.5V to +40V input voltage range. Connect a 0.1μ F compensation capacitor from V5 to ground. Shorting V5 to ground disables the thermal shutdown. When EN is low, V5 is off. V5 stays on during PWM dimming.

Thermal Protection

The MAX16803 enters a thermal-shutdown mode in the event of overheating. This typically occurs in overload or output short-circuit conditions. If the junction temperature exceeds $T_J = +155$ °C (typ), the internal thermal-protection circuitry turns off the series pass device. The MAX16803 recovers from thermal-shutdown mode once the junction temperature drops by 23°C (typ). The part therefore protects itself by thermally cycling in the event of a short-circuit or overload condition.

Applications Information

Programming the LED Current

The MAX16803 uses a sense resistor across CS+ and CS- to set the LED current. The differential sense amplifier connected across R_{SENSE} provides ground-loop immunity and low-frequency noise rejection. The LED current is given by

 $I_{LED} = V_{SENSE} / R_{SENSE}$



MAX16803

Input-Voltage Considerations

For proper operation, the minimum input voltage must always be:

 $V_{IN(MIN)} \ge V_{RSENSE(MAX)} + V_{FT(MAX)} + \Delta V_{DO(MAX)}$

where V_{FT(MAX)} is the total forward voltage of all series connected LEDs. The minimum operating voltage of the device is +6.5V. The device operates below +6.5V; however, output current may not meet the full regulation specification (see the *Typical Operating Characteristics*).

Low-Frequency PWM Dimming at the Output The MAX16803 provides pulsed or chopped current

dimming input (DIM). The other method is to connect DIM to V5 and EN to IN and pulse EN. Both methods generate a regulated-amplitude PWM current (variable duty cycle) that can provide control over the LED brightness (see Figures 1 and 2).

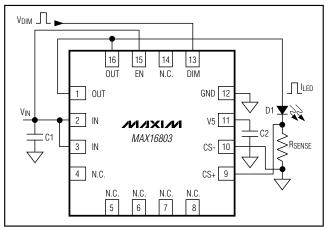


Figure 1. Dimming with EN Connected to V_{IN} at a Constant Voltage and DIM Pulsed

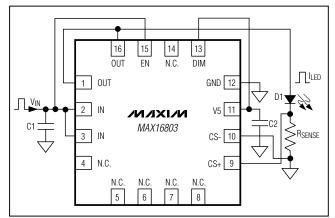


Figure 2. Dimming with DIM Connected to V5, EN Connected to $V_{\mbox{IN}}$

Paralleling Multiple MAX16803s to Drive One High-Power LED

For applications that require more than 350mA of LED current, two or more MAX16803s can be paralleled (see Figure 3). V_{CS}- should not exceed 4.1V.

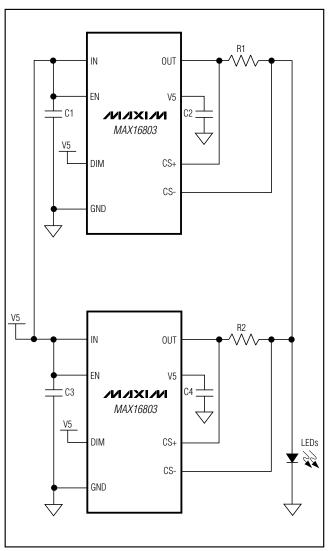


Figure 3. Paralleling MAX16803s

Two Brightness Levels for TAIL/STOP Lights

Figure 4 shows PWM dimming operation for the MAX16803 with an ICM7555 timer. The ICM7555 provides adjustable duty cycle using two external resistors and a capacitor. In TAIL operation, the output of the ICM7555 feeds into DIM and lights up the LEDs. The LED's brightness depends on the duty cycle of the ICM7555. When VSTOP is present, DIM is pulled up to VSTOP. The PWM dimming operation is disabled and the LEDs light up to full brightness. See the ICM7555 data sheet for formulas to calculate the dimming frequency and the duty cycle.

LED Current Thermal Foldback

With a minimum number of external components, the MAX16803 provides LED current thermal foldback using a negative temperature coefficient (NTC) thermistor. Figure 5 shows a thermistor connected to V5 and the CS+ of the MAX16803. As the temperature increases, the voltage drop across R2 increases causing the LED current to decrease.

 $I_{LED} = [V_{SENSE} - [R2 / (R2 + RT)] \times V5] / R1$

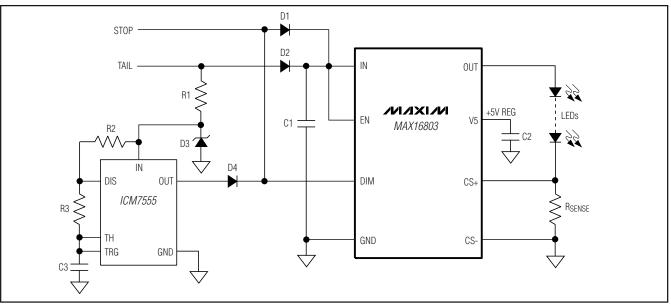


Figure 4. PWM Dimming Operation with ICM7555 Timer

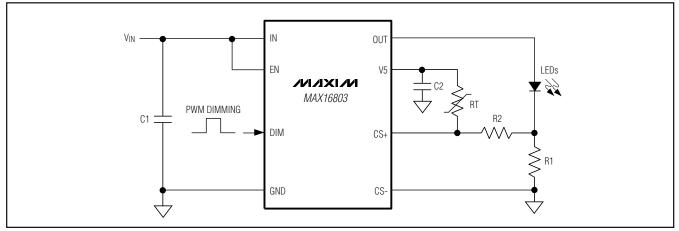


Figure 5. LED Current Thermal Foldback with an NTC Thermistor

M/XI/M

Other Applications

The application circuit in Figure 6 implements a twolevel brightness current for TAIL/STOP lights. In TAIL operation, Q1 is off and the R1 sets the output current. In STOP operation, Q1 turns on and the output current is set by a parallel combination of R1 and R2. Figure 7 shows an application circuit with the MAX16803 using a single BJT to provide high output current. For proper operation:

 $\label{eq:VIN(MIN)} $$ VCESAT(MAX) + VFT(MAX) + VRSENSE$ where $$ VCESAT(MAX)$ is the maximum saturation voltage of the external BJT. $$$

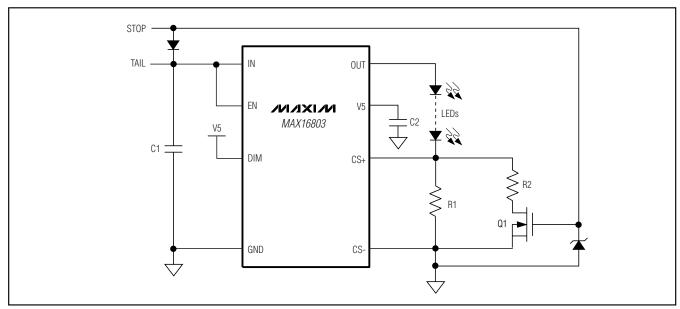


Figure 6. Two Brightness Level with Current Level Switch for Tail/Stop Lights

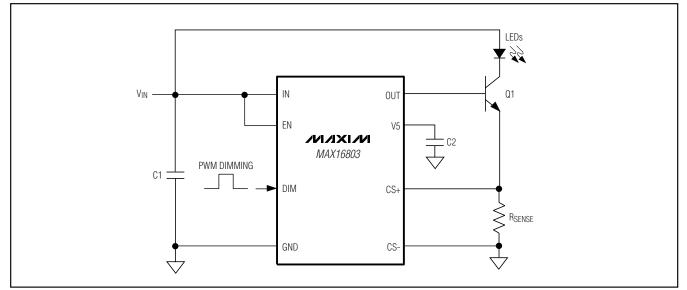


Figure 7. Increased LED Current (Amper Range) with a Single BJT

MAX16803

Multichannel HB LED Driver

Figure 8 shows an array of MAX16803s with independent DIM control. The MAX5094C, a current-mode PWM controller, provides the input power to each LED driver preregulated voltage to multiple MAX16803 drivers.

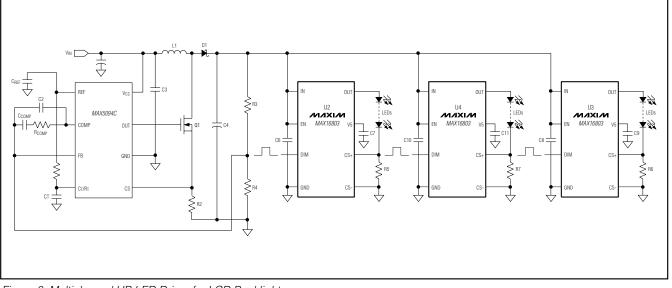
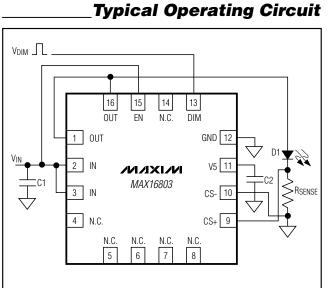


Figure 8. Multichannel HB LED Driver for LCD Backlight

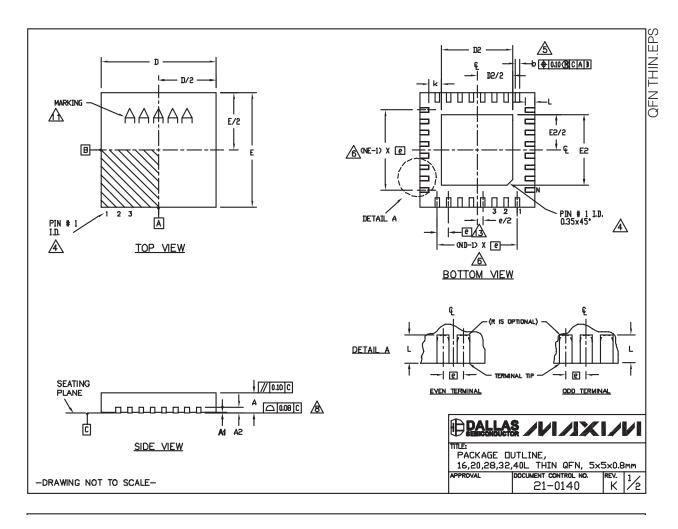


Chip Information

PROCESS: BiCMOS

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to <u>www.maxim-ic.com/packages</u>.)



Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to **www.maxim-ic.com/packages**.)

| | | COM | MON DIMENSIONS | s | | | | | | | EX | POSED | PAD | ARIAT | IONS | |
|----------|--|--|---|--|--------------|---------------------------|------------------------|----------|---|----------|---------------------|-----------------|--------------|---------|---------|------|
| PKG. | 16L 5×5 | 20L 5×5 | 28L 5×5 | 32L | 5x5 | | | ×5 | 1 | PKG. | | D2 | | | E2 | |
| SYMBOL | | | MIN. NOM. MAX. | | | | | | | CODES | MIN. | NDM. | MAX. | MIN. | NDM. | MAX. |
| A | | | 0.70 0.75 0.80 | | _ | | _ | |] | T1655-2 | 3.00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| A1 | 0 0.02 0.05 | | 0 0.02 0.05 | | | | | | | T1655-3 | 3.00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| A2 | 0.20 REF. | 0.20 REF. | 0.20 REF. | 0.20 R | | | 0 REF | | | T1655N-1 | 3.00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| b | | | 0.20 0.25 0.30 | | | | | | | T2055-3 | 3.00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| D E | | | 4.90 5.00 5.10 | | | | | | | T2055-4 | 3.00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| e | 0.80 BSC. | 0.65 BSC. | 4.90 5.00 5.10 0.50 BSC. | 0.50 I | - | | 40 BS0 | | 1 | T2055-5 | 3.15 | 3.25 | 3.35 | 3.15 | 3.25 | 3.35 |
| <u>е</u> | 0.25 | | | 0.25 - | <u> </u> | 0.25 | _ | _ | | T2055M-5 | 3.15 | 3.25 | 3.35 | 3.15 | 3.25 | 3.35 |
| | | | 0.45 0.55 0.65 | | - | | _ | | | T2855-3 | 3.15 | 3.25 | 3.35 | 3.15 | 3.25 | 3.35 |
| N | 16 | 20 | 28 | 32 | 1 0100 | 5.00 | 40 | | | T2855-4 | 2.60 | 2.70 | 2.80 | 2.60 | 2.70 | 2.80 |
| ND | 4 | 5 | 7 | 8 | | | 10 | _ | | T2855-5 | 2.60 | 2.70 | 2.80 | 2.60 | 2.70 | 2.80 |
| NE | 4 | 5 | 7 | 8 | | | 10 | | | T2855-6 | 3.15 | 3.25 | 3.35 | 3.15 | 3.25 | 3.35 |
| JEDEC | VHHB | WHHC | WHHD-1 | VHHD | -2 | - | | | | T2855-7 | 2.60 | 2.70 | 2.80 | 2.60 | 2.70 | 2.80 |
| | | | | | | | | | | T2855-8 | 3.15 | 3.25 | 3.35 | 3.15 | 3.25 | 3.35 |
| | | | | | | | | | | T2855N-1 | 3.15 | 3.25 | 3.35 | 3.15 | 3.25 | 3.35 |
| NUTES | | | | | | | | | | T3255-3 | 3.00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| | | DLERANCING CON | Form to asme y | 14.5N-1994 | | | | | | T3255-4 | 3.00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| 2. Al | LL DIMENSIONS A | RE IN MILLIMETE | RS. ANGLES ARE | IN DEGRE | ES. | | | | | T3255M-4 | 3.00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| 3, N | IS THE TOTAL N | iumber of term | inals, | | | | | | | T3255-5 | 3.00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| <u> </u> | | | TERMINAL NUMBE | | | | | | | T3255N-1 | 3,00 | 3.10 | 3.20 | 3.00 | 3.10 | 3.20 |
| | | | DETAILS OF TER | | | | | | | T4055-1 | 3.40 | 3.50 | 3.60 | 3.40 | 3.50 | 3.60 |
| | | | vithin the Zon D or Marked Fe | | ED, IH | E IER | MINAL | #1 | I | T4055-2 | 3,40 | 3,50 | 3.60 | 3.40 | 3.50 | 3.60 |
| | Imension & Appl 25 mm and 0.30 d and ne refer Epopulation is oplanarity appl Raving conform 2855-3, t2855-6 Arpage Shall N Arking is for P JMBER of Leads | IES TO METALLI INA FROM TERMIN TO THE NUMBER POSSIBLE IN A S IES TO THE EXF S TO JEDEC MO2 S, T4055-1 AND 1 OT EXCEED 0.10 ACKAGE DRIENTA SHOWN ARE FOR | ZED TERMINAL A AL TIP. DF TERMINALS SYMMETRICAL FA POSED HEAT SINK 20, EXCEPT EXPL 14055-2. | in each i Shidn. Shidn. (Slug As Dsed Pad : Dnly. Ly. | vell Vell | e sidi As ti Cion f | e resp He ter Or | RMINALS. | | P | E ACKA 5,20,2 | GE Ol 18,32, | JTLIN 40L | Ξ. Ξ. | QFN, | 5×5 |
| | | | | | | | | | | IAPPE | | | DOCUME | INT CON | ITROI N | n I |

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